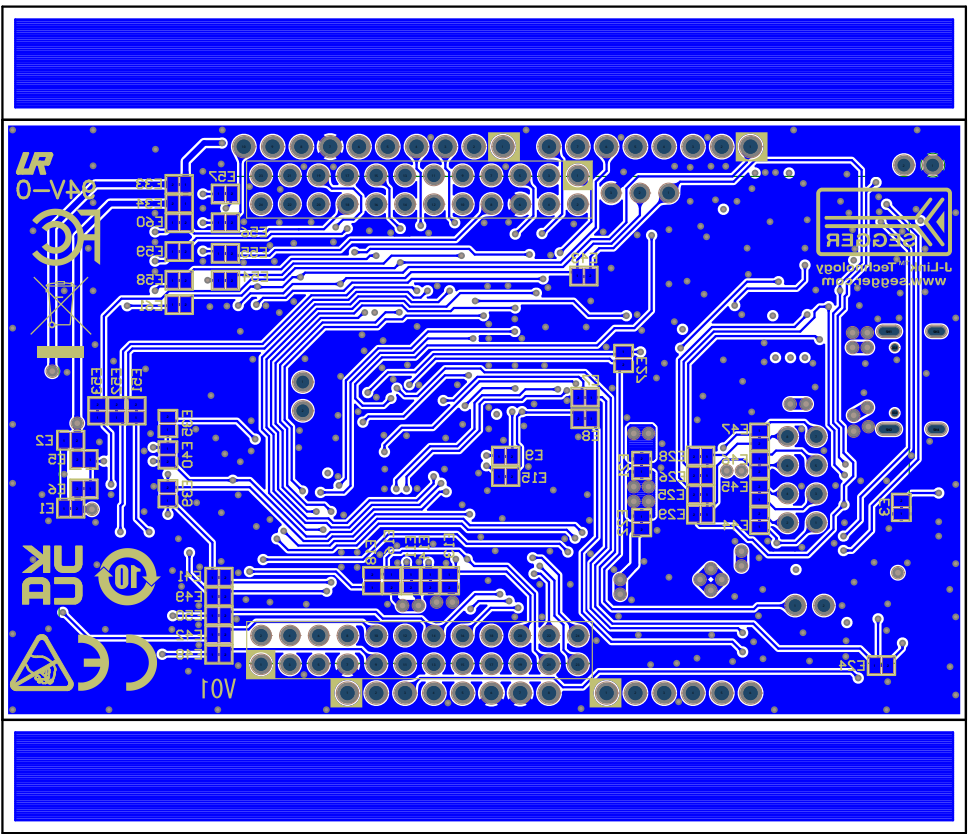


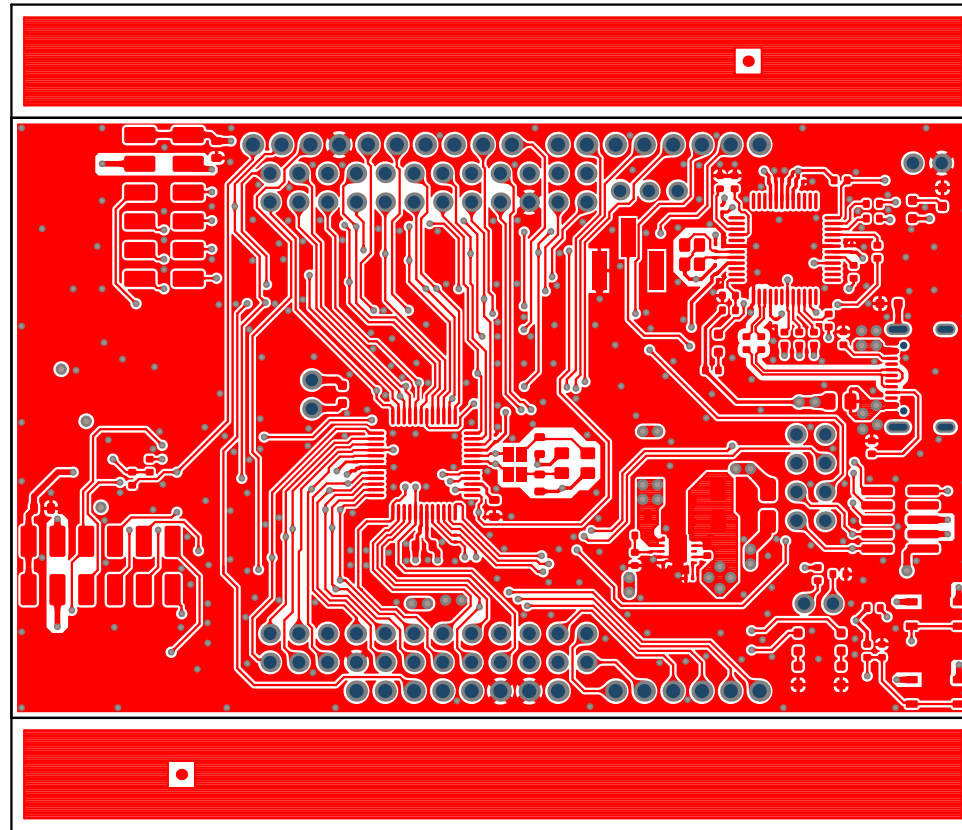
Top Layer

Top Solder

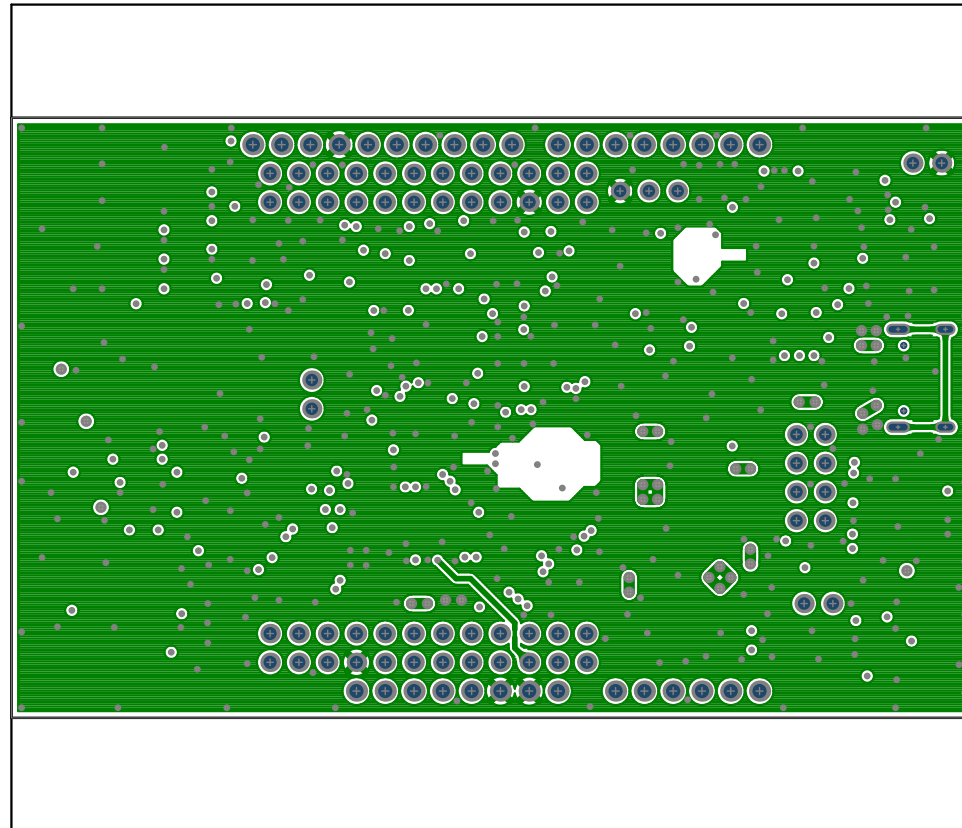
Top Overlay



Bottom Layer
Bottom Solder
Bottom Overlay

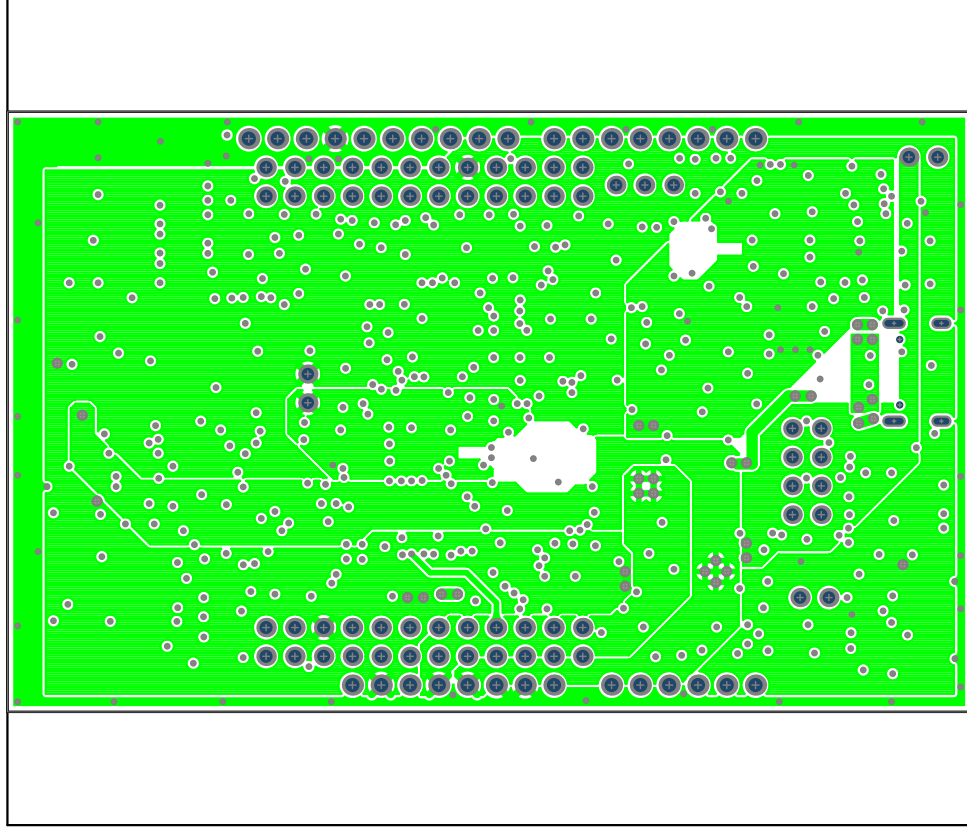


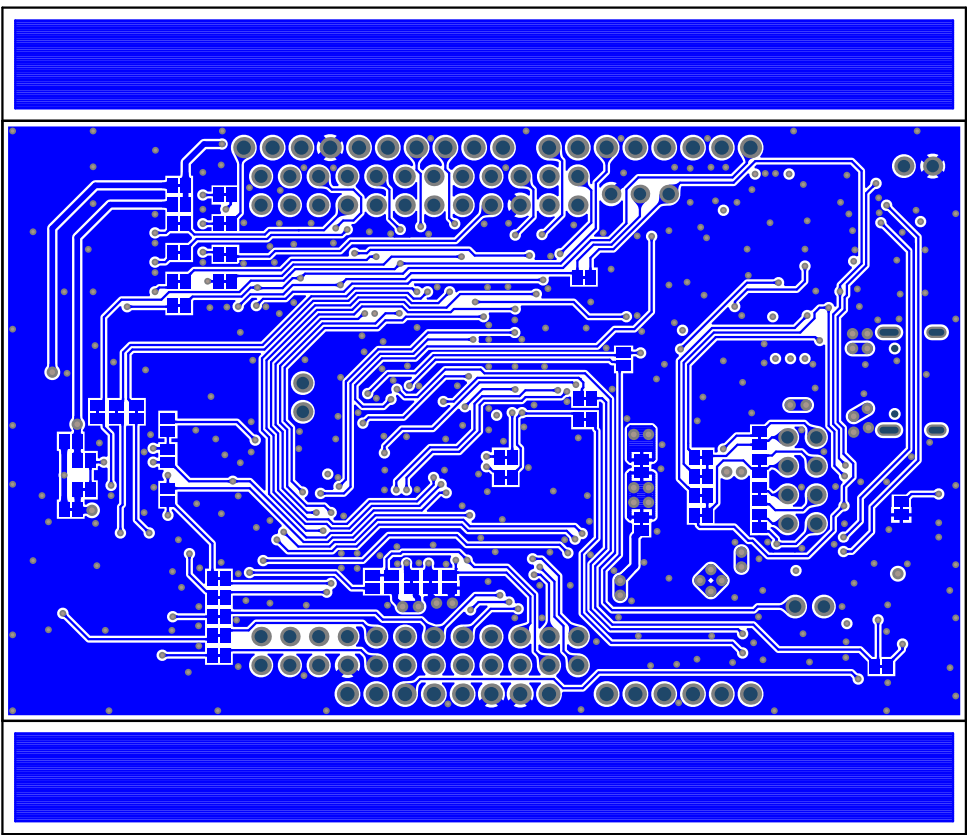
Top Layer



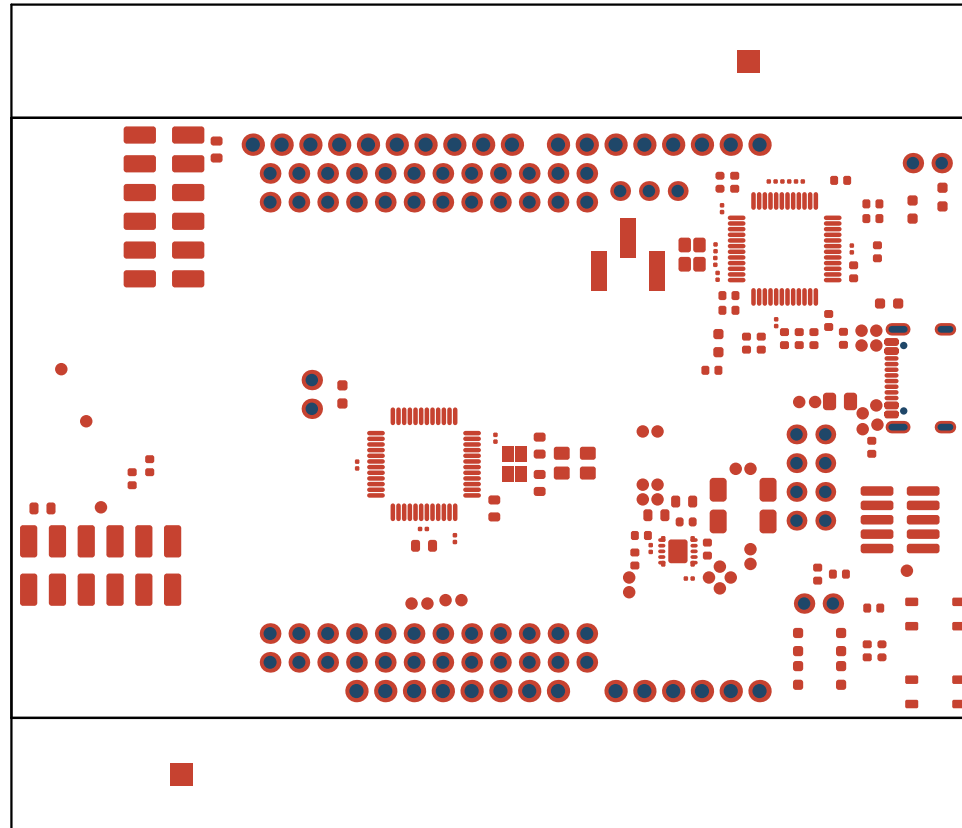
L2 Layer

Layer 3

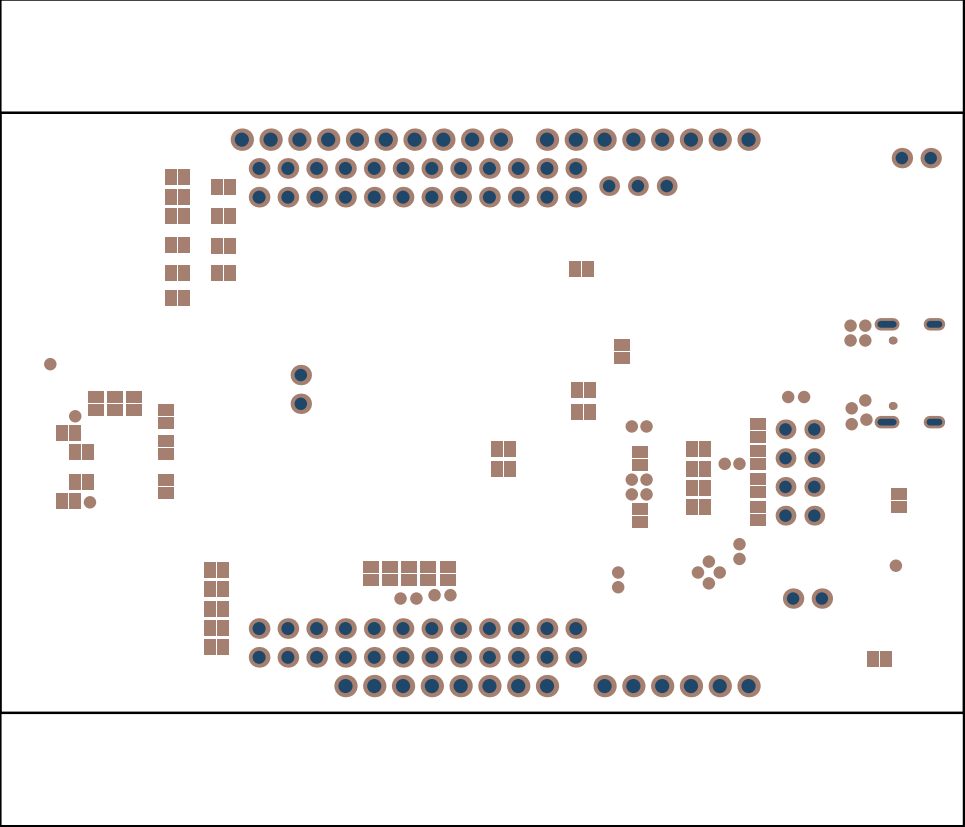




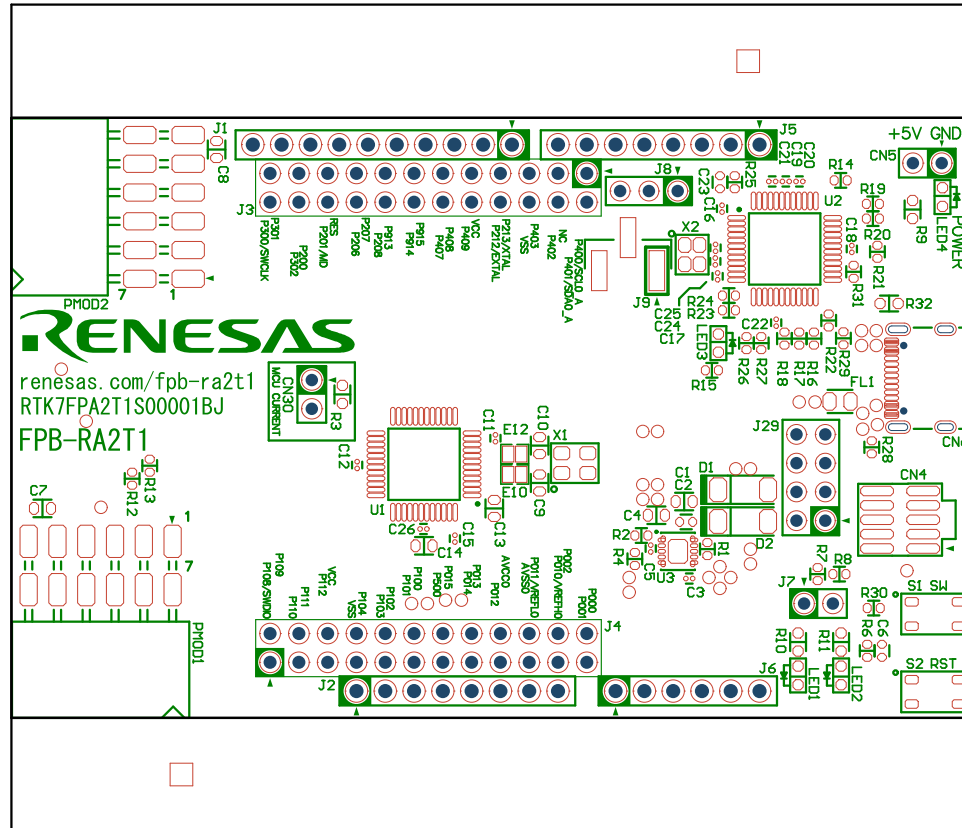
Bottom Layer



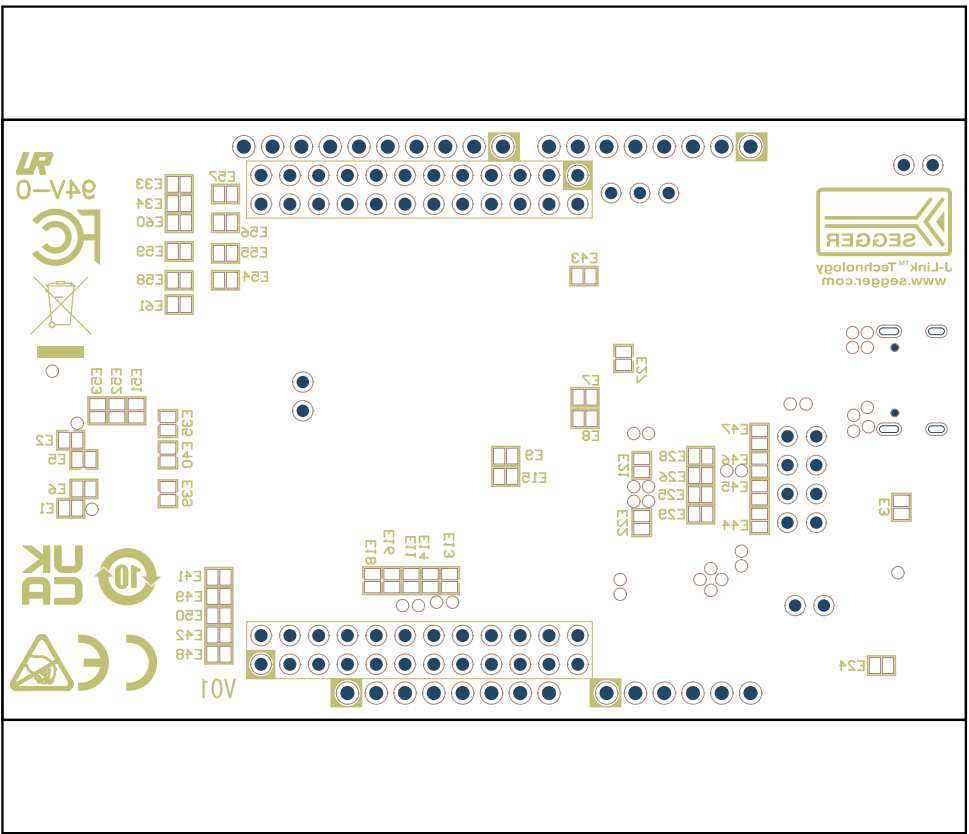
Top Solder



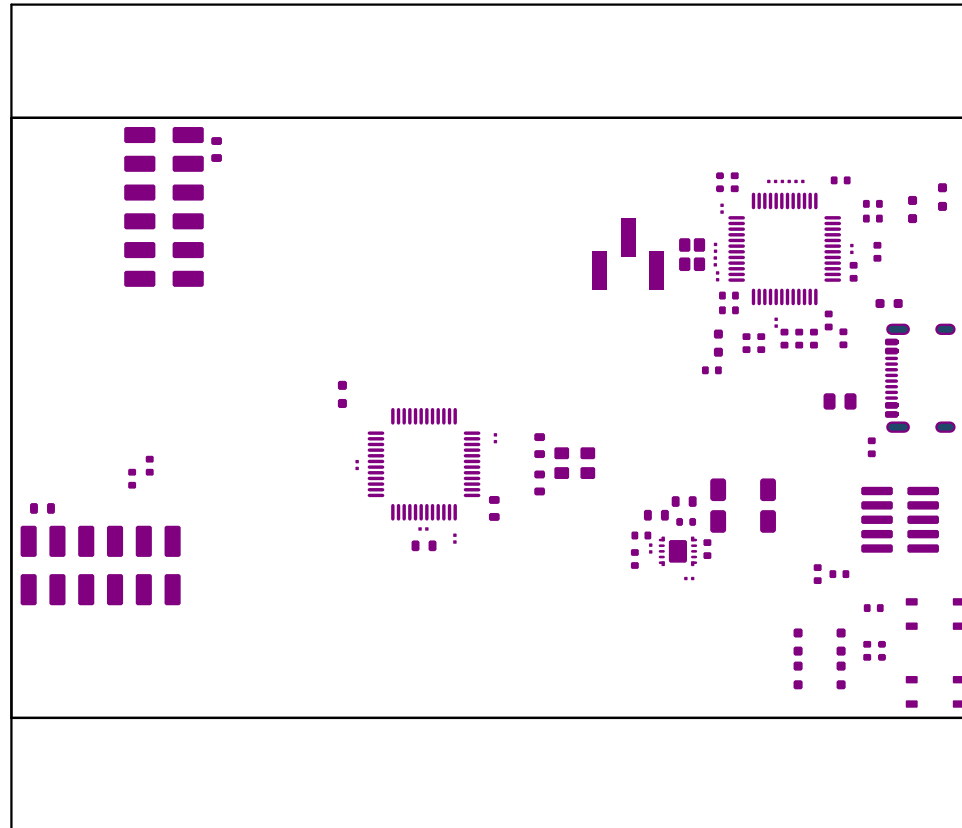
Bottom Solder



Top Solder
Top Overlay

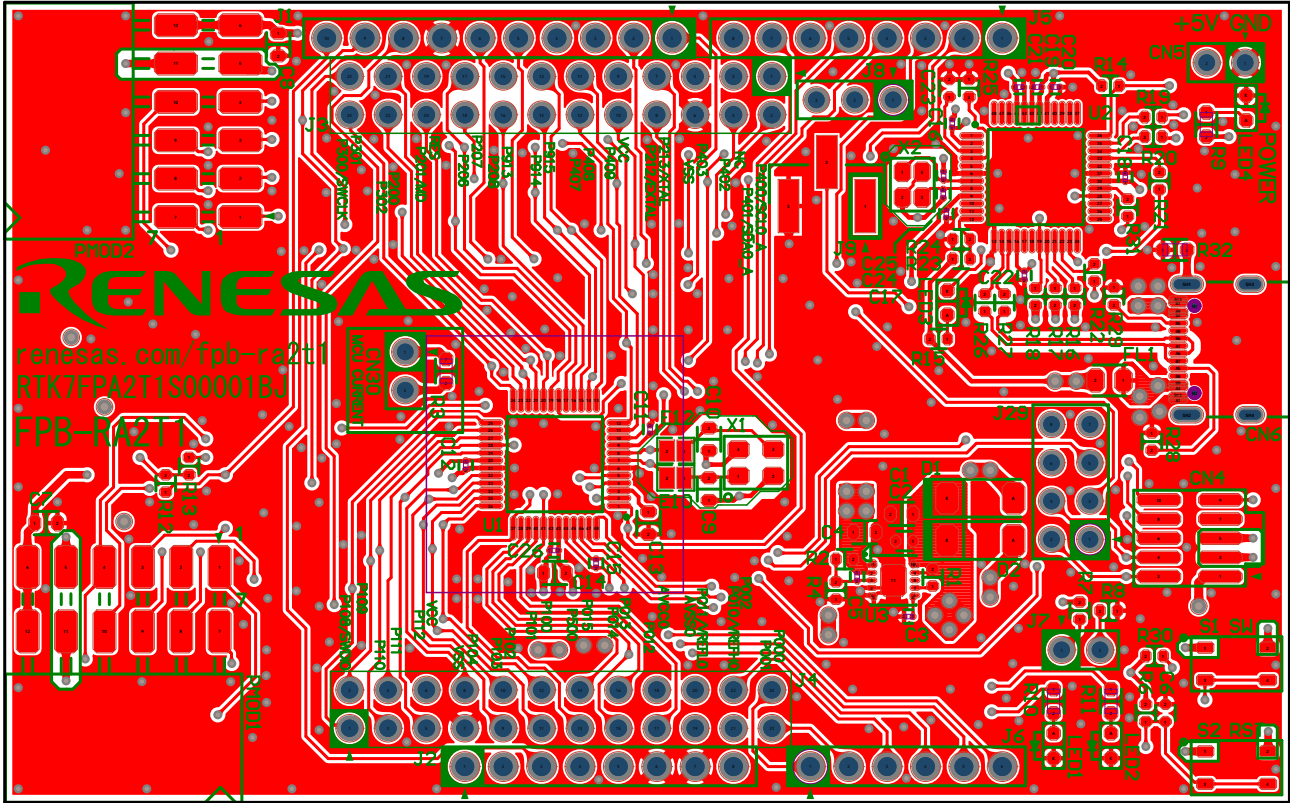


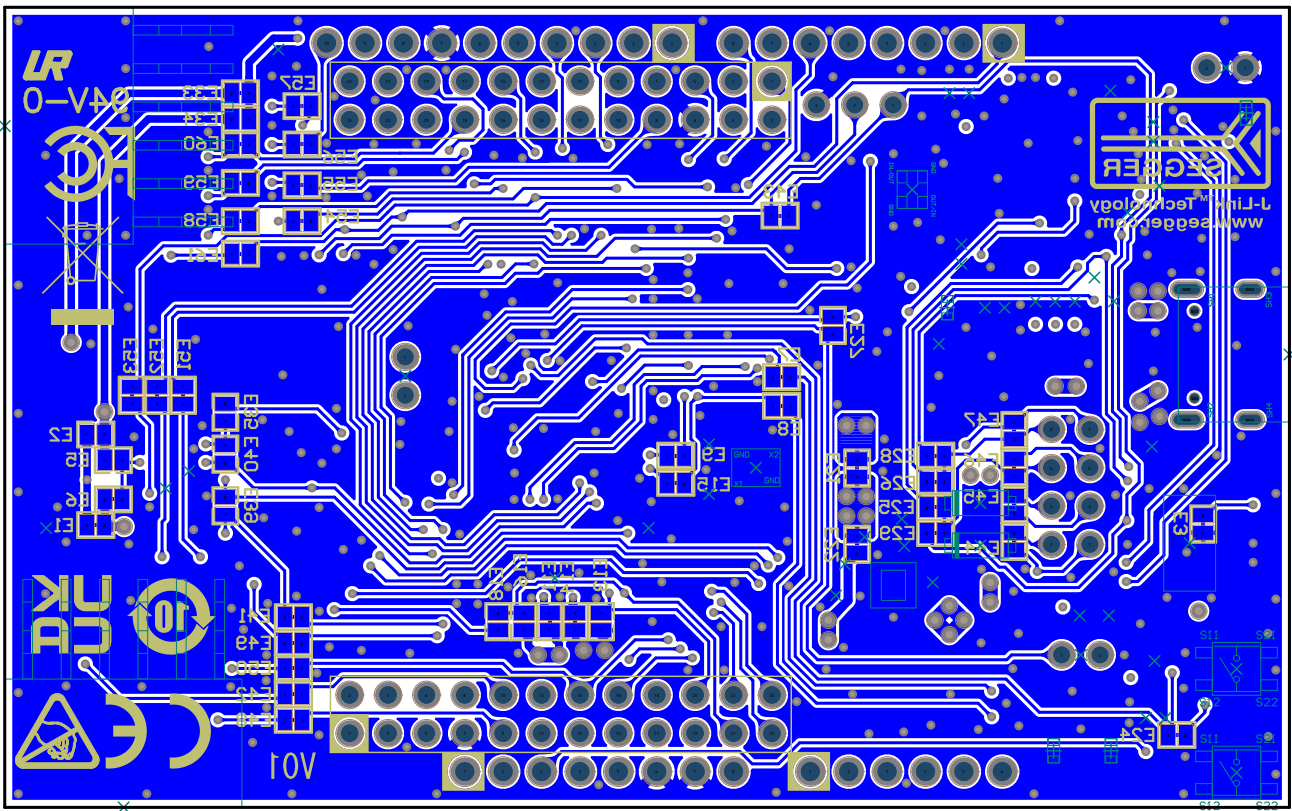
Bottom Solder
Bottom Overlay

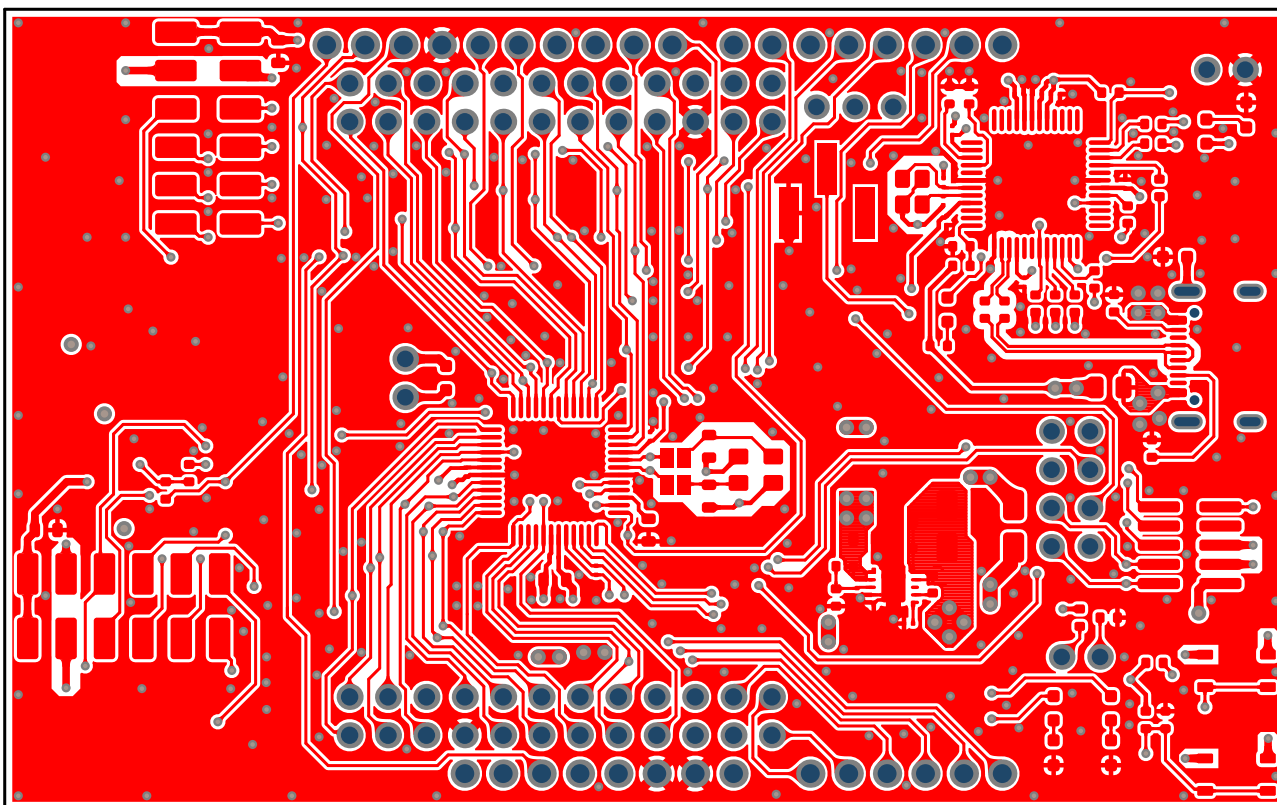


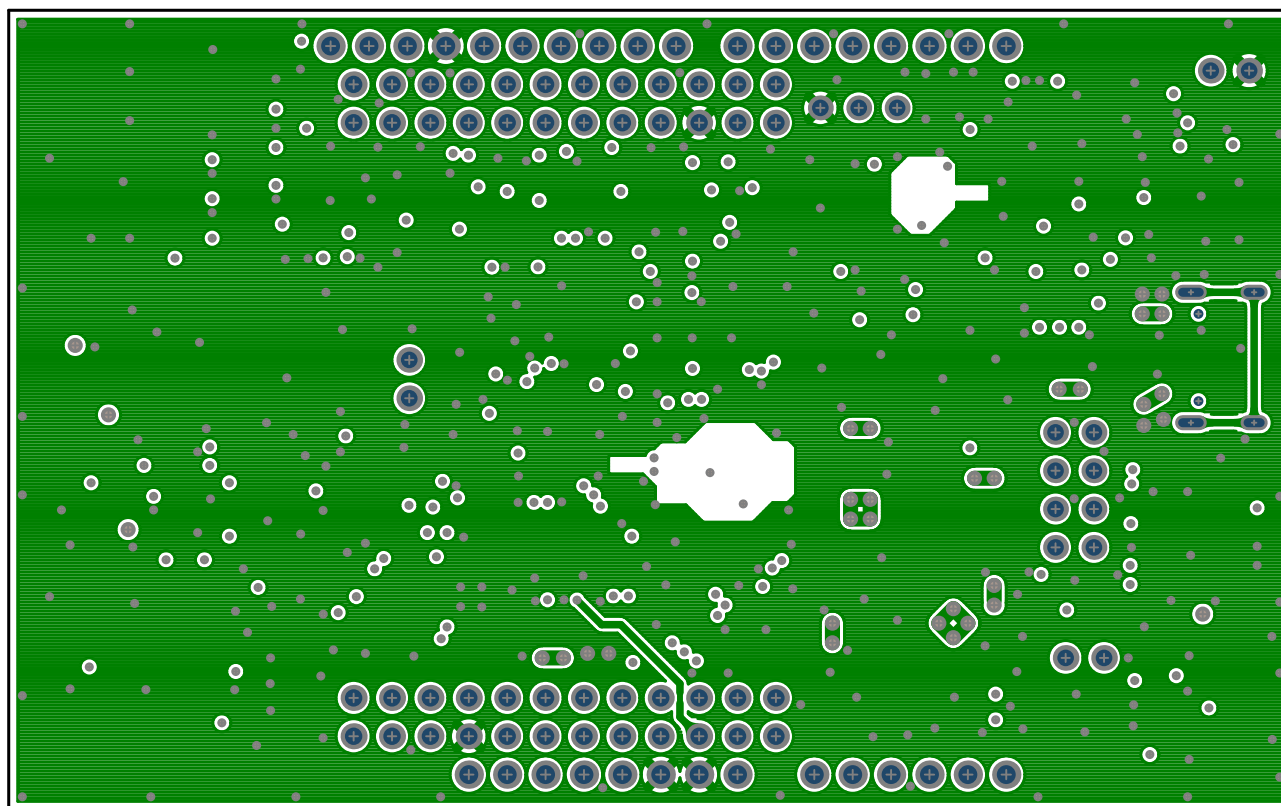
Top Paste

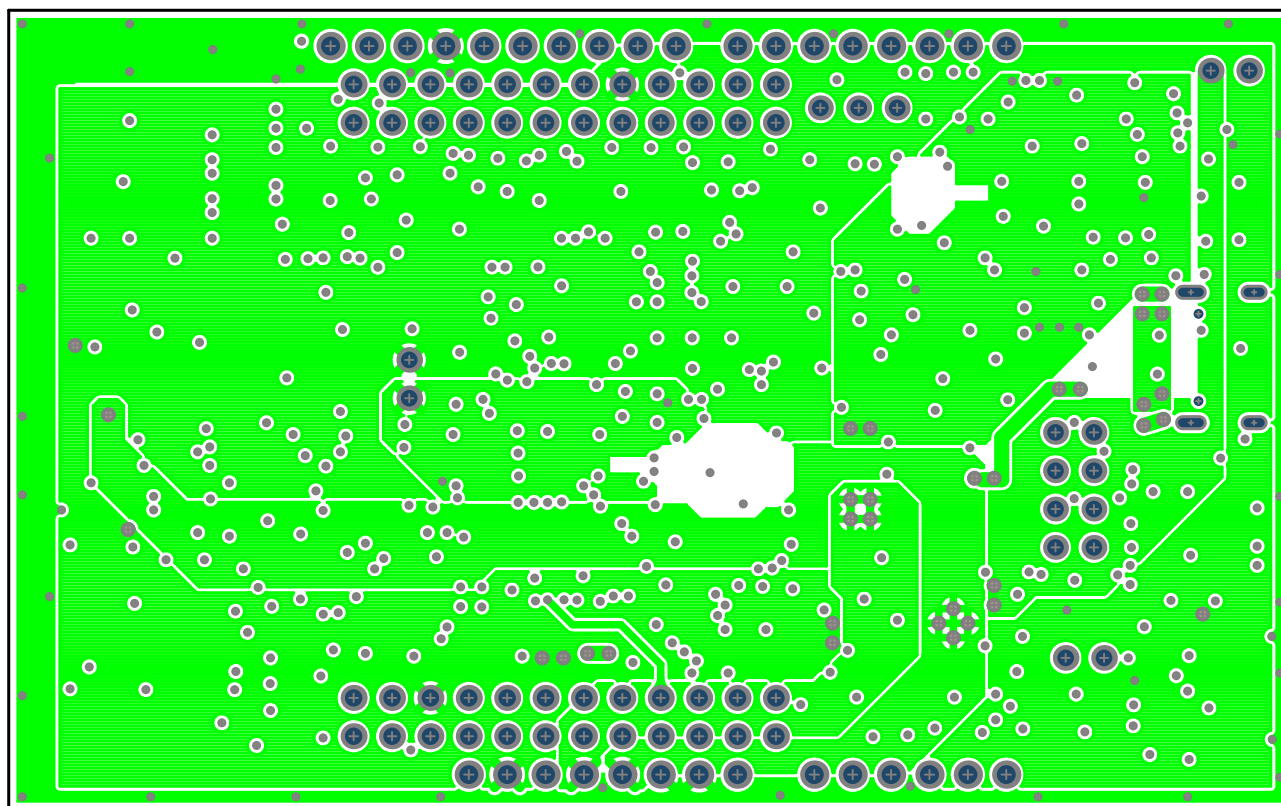
Bottom Paste

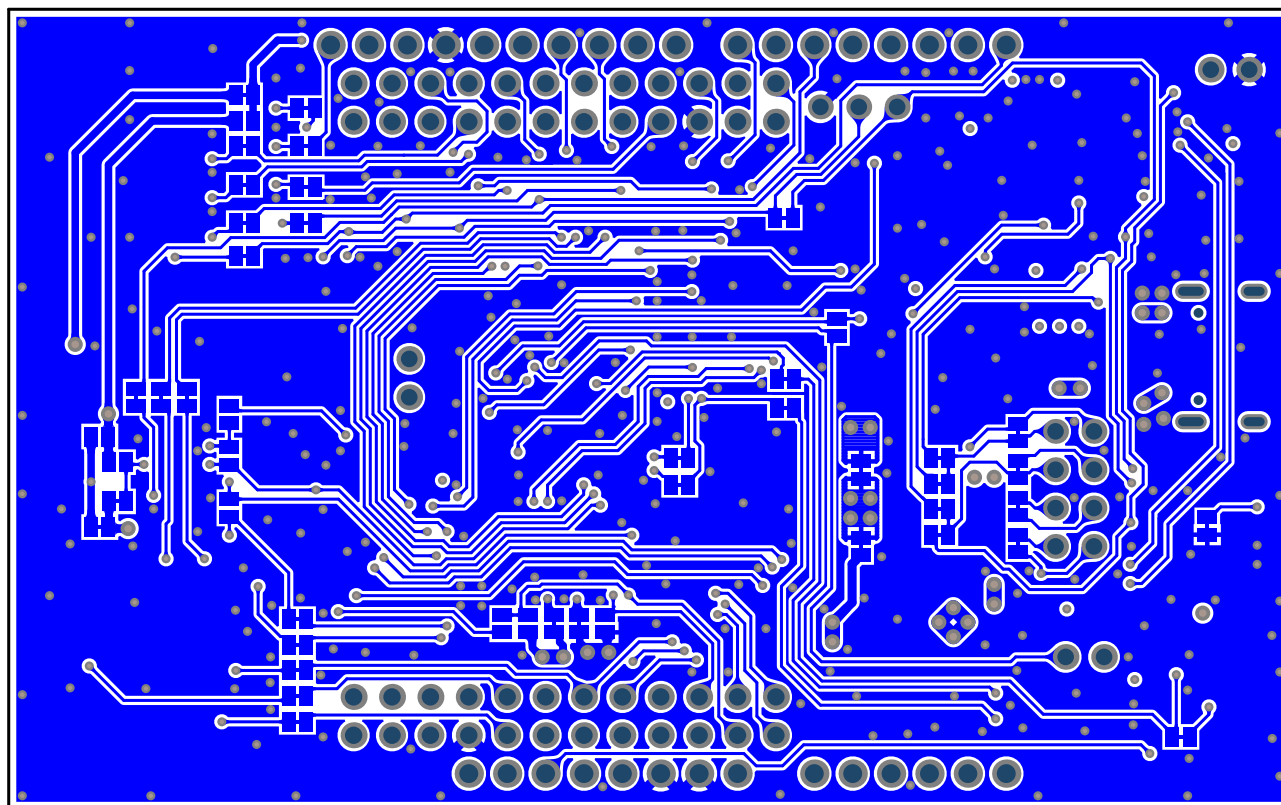


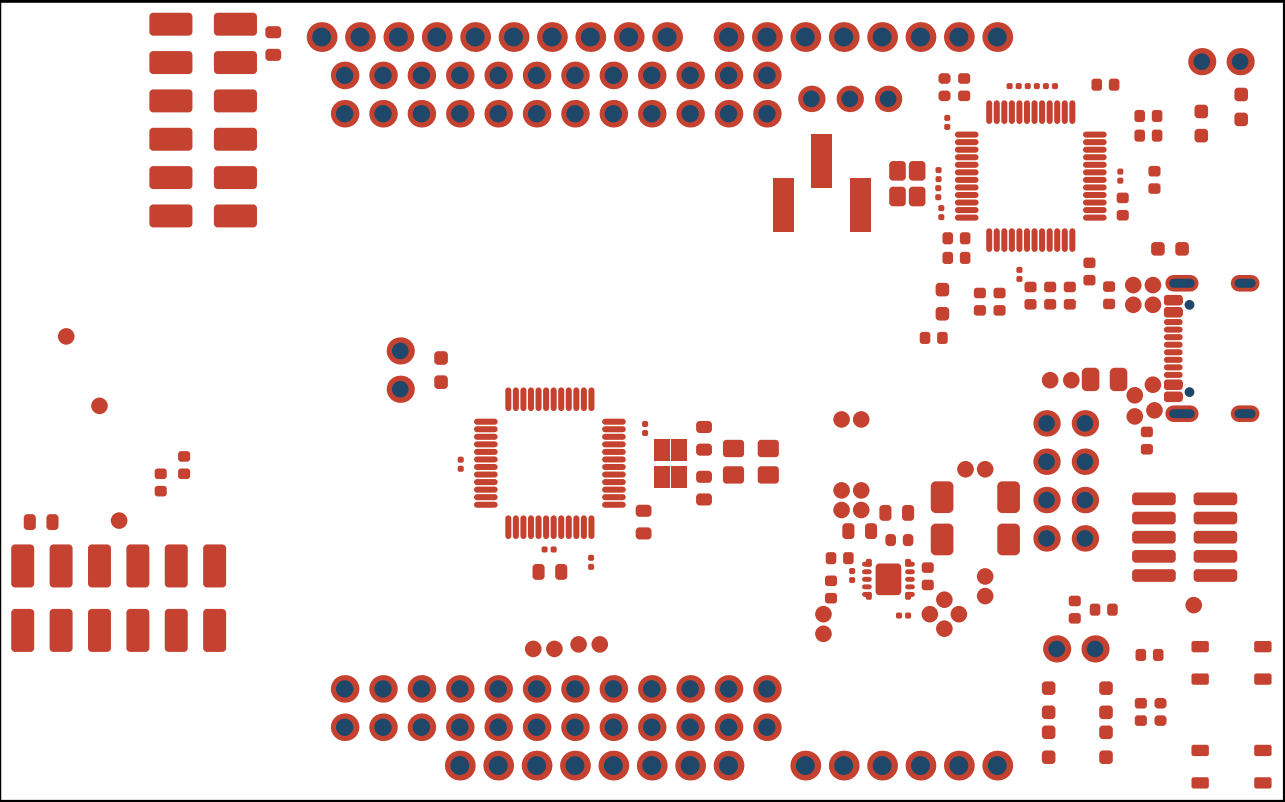


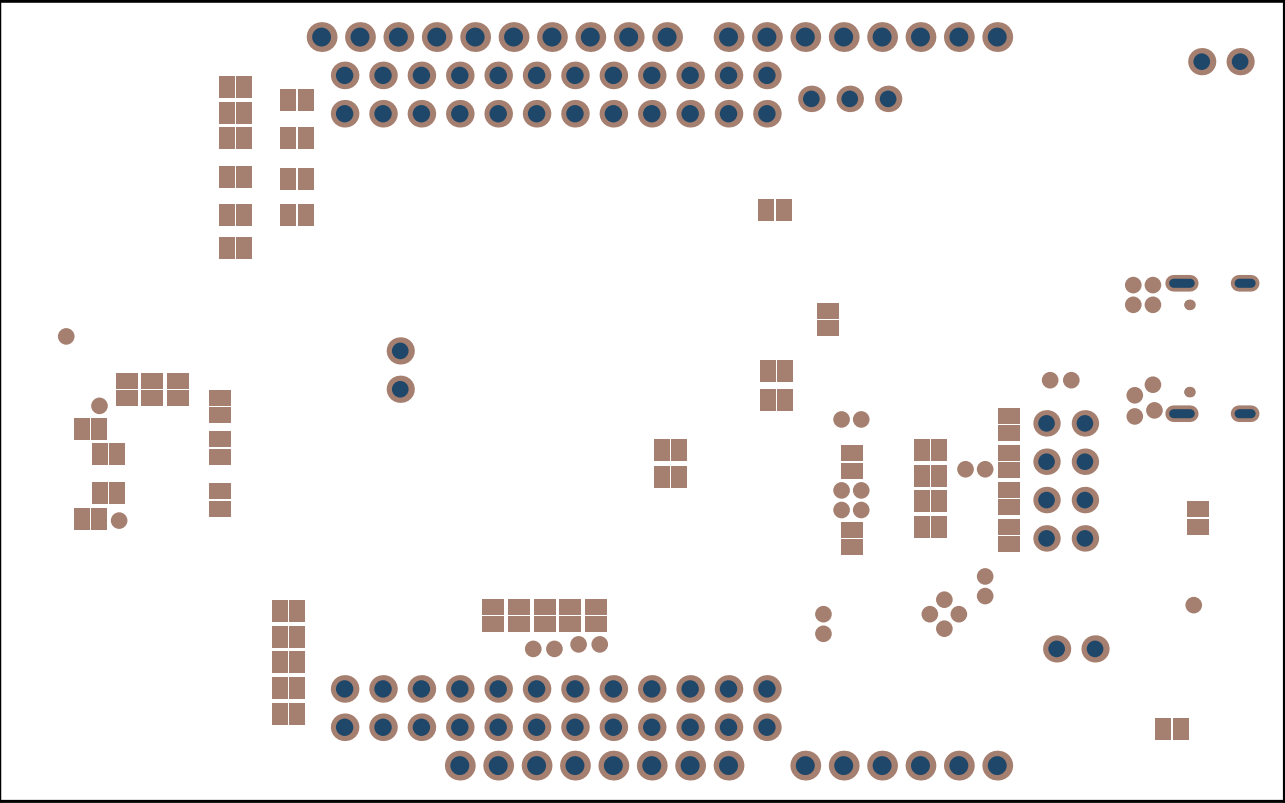


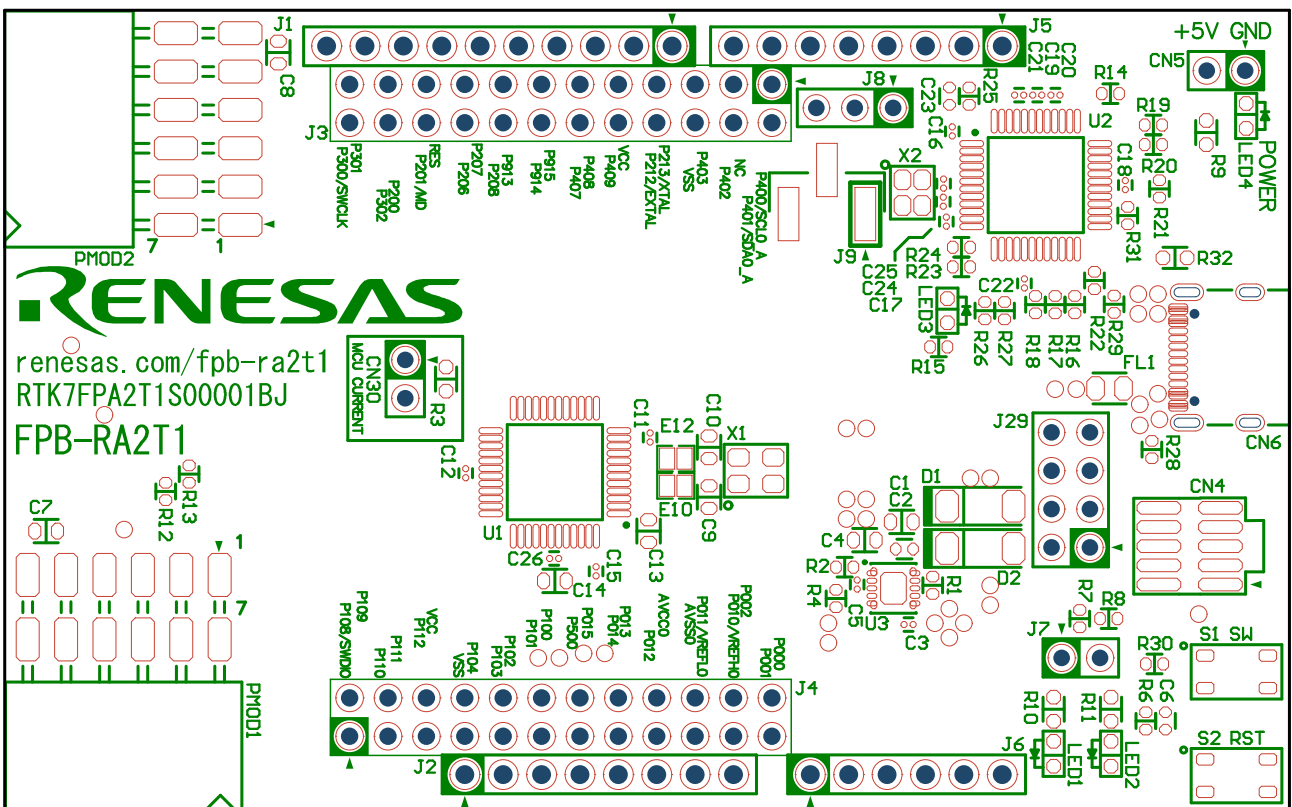


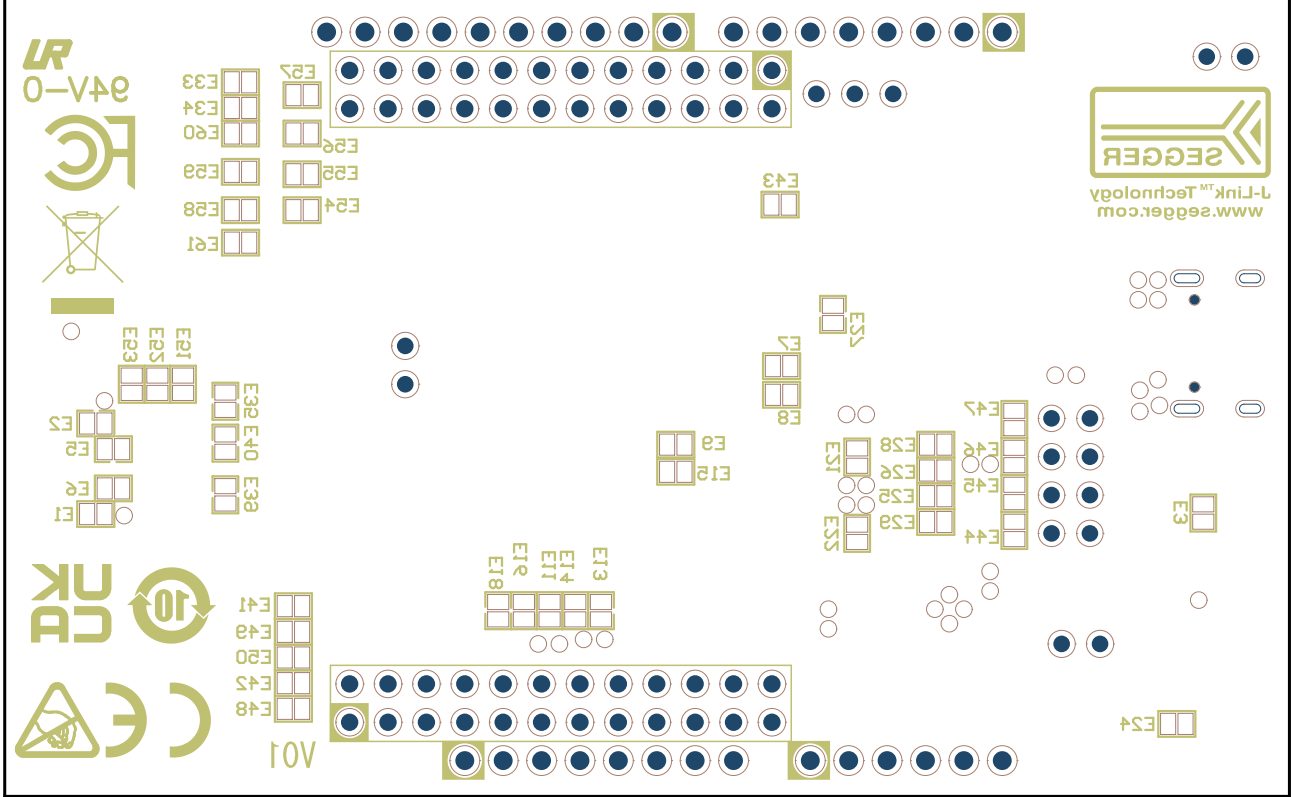


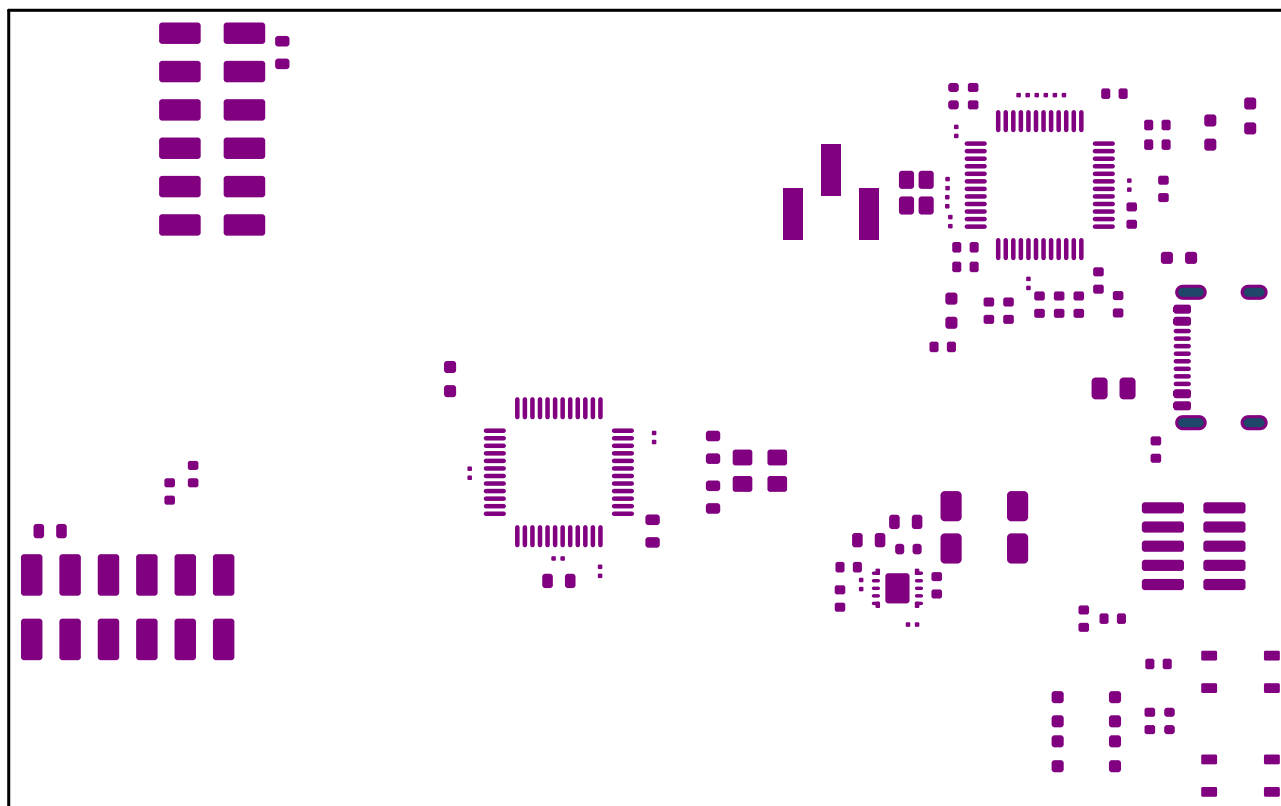


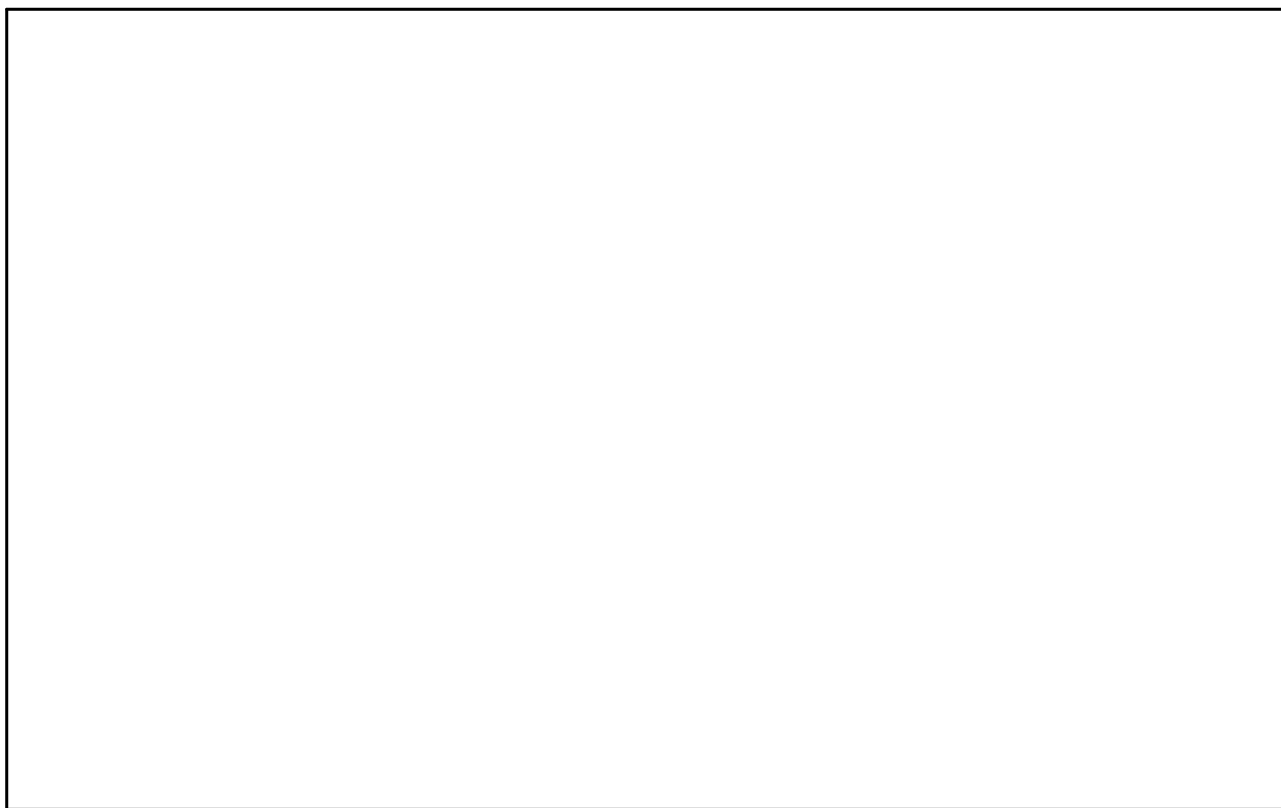






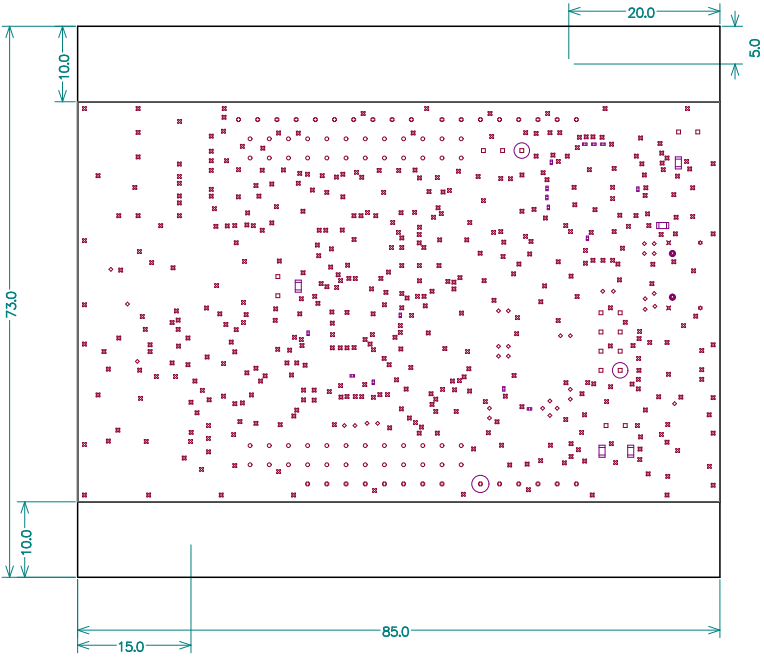






Symbol	Count	Hole Size	Plated	Hole Type	Drill Layer Pair	Hole Tolerance	Hole Length
⊗	454	0.30mm	PTH	Round	Top Layer - Bottom Layer		-
◇	34	0.50mm	PTH	Round	Top Layer - Bottom Layer		-
☆	2	0.60mm	PTH	Slot	Top Layer - Bottom Layer		1.40mm
⊗	2	0.60mm	PTH	Slot	Top Layer - Bottom Layer		1.70mm
▽	2	0.65mm	NPTH	Round	Top Layer - Bottom Layer		-
□	17	1.10mm	PTH	Round	Top Layer - Bottom Layer		-
○	48	1.15mm	PTH	Round	Top Layer - Bottom Layer		-
⊗	32	1.25mm	PTH	Round	Top Layer - Bottom Layer		-
	591 Total						

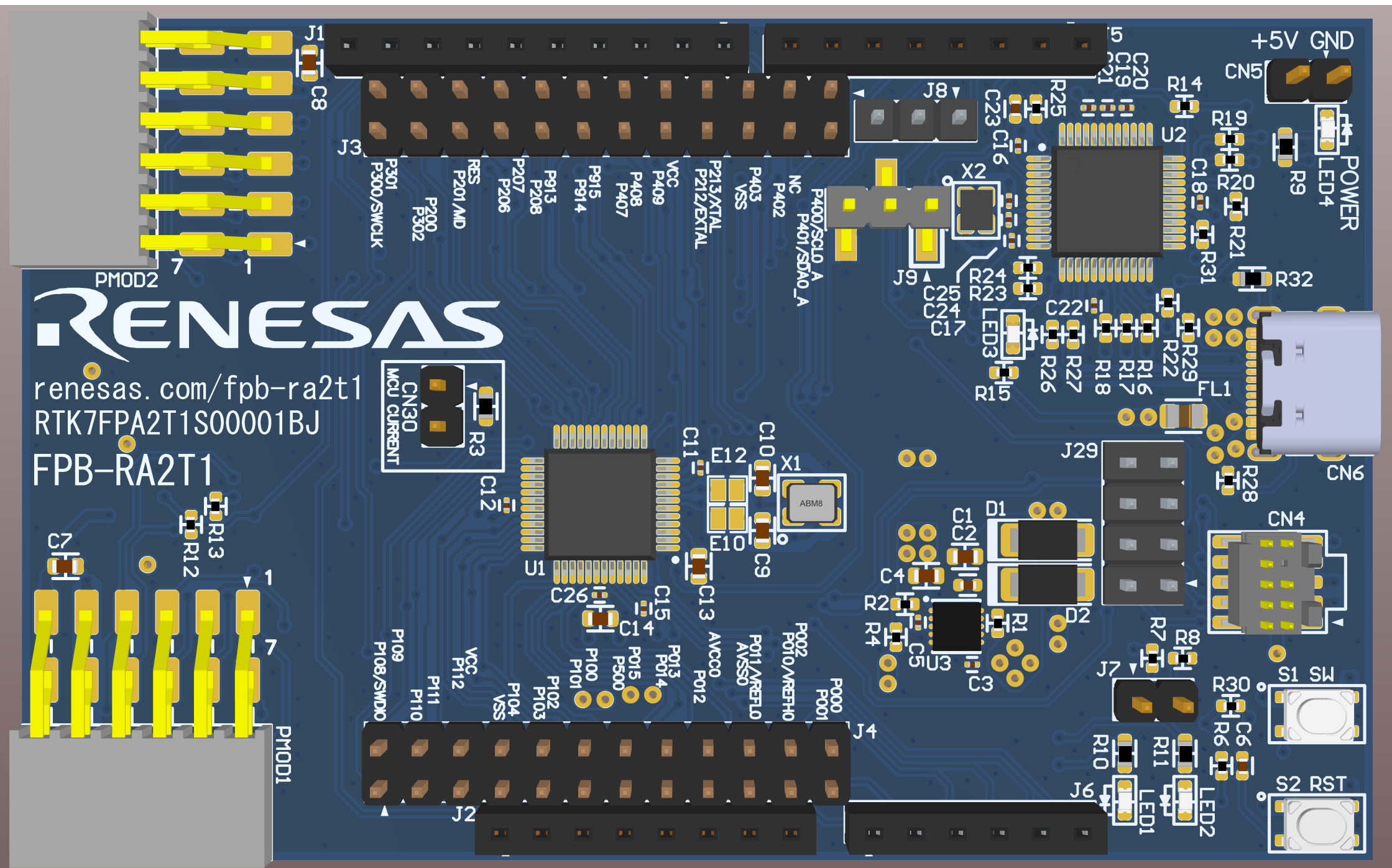
Slot definitions : Routed Path Length = Calculated from tool start centre position to tool end centre position.
Hole Length = Routed Path Length + Tool Size = Slot length as defined in the PCB layout



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RTK7FPA2T1S00001BJ

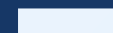
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V01



E43

